CSCI 1470

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Wednesday, 2/12/25

Deep Learning

Day 10: Introduction to Convolutions

Recap: MLPs

1.

2.

3.



A Brief History of Al with Deep Learning



https://medium.com/@lmpo/a-brief-history-of-ai-with-deep-learning-26f7948bc87b

What has happened in the last 15 years?

What has changed?

- 1. Power and efficiency of compute (GPUs)
- 2. Availability of data (the internet)
- 3. New Architectures (e.g., CNNs, Transformers)



Issues with MLPs

- 1. Resource Intensive
- 2. Difficult to incorporate certain types of information
- 3. (and more)

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GPUs to the rescue!



- Graphics Processing Units
- GPUs are really good at computing mathematical operations in parallel!
- Matrix multiplication == many independent multiply and add operations

Easily parallelizable

GPUs are great for this!





CPU v/s GPU





GPU-Parallel Acceleration

- User code (*kernels*) is compiled on the *host* (the CPU) and then transferred to the *device* (the GPU)
- Kernel is executed as a *grid*
- Each grid has multiple *thread blocks*
- Each thread block has multiple *warps*

A warp is the basic schedule unit in kernel execution

A warp consists of 32 threads

Compute Unified Device Architecture is a parallel computing platform and application programming interface (API)

CUDA compute model



GPU-Parallel Acceleration

CUDA compute model

| Host Kernel 0 |
|--|
| Device |
| Grid 0 Block 0 Block 1 Block 2 Block 3 Block 4 Block 5 Block 6 Block 7 Block 8 Block 9 Block 10 Block 11 Block 12 Block 13 Block 14 Block 15 |
| Thread 0Thread 2Thread 3Thread 4Thread 6Thread 7Block 13Thread 8Thread 9Thread 10Thread 11Thread 12Thread 13Thread |
| Thread 32Thread 33Thread 34Thread 35Thread 36Thread 37Thread 38Block 13Thread 40Thread 41Thread 42Thread 43Thread 44Thread 45Thread 46Thread 47Thread 48Thread 49Thread 50Thread 51Thread 52Thread 53Thread 54Thread 55 |
| Thread Thread Thread Thread Thread Thread Thread Thread 62 63 |

- Programmer decides how they want to parallelize the computation across grids and blocks
 - Modern deep learning frameworks take care of this for you
- CUDA compiler figures out how to schedule these units of computation on to the physical hardware

GPU-Parallel Acceleration

Any questions?

CUDA compute model



- Upshot: order of magnitude speedups!
- Example: training CNN on CIFAR-10 dataset

| Device | Speed of training, examples/sec |
|----------------------|------------------------------------|
| 2 x AMD Opteron 6168 | 440 |
| i7-7500U | 415 |
| GeForce 940MX | 1190 |
| GeForce 1070 | 6500 |

From:

https://medium.com/@andriylazorenko/tensorflow-performance-test-cp u-vs-gpu-79fcd39170c AMD GPUs are competitive for gaming and graphics, why not for AI?



- CUDA is far better than competitors (AMD)

(With a benchmarking tool made by AMD)

Easier to use

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- Better optimization
- AMD makes GPUs for graphics, NVIDIA makes GPUs for AI

CUDA is Still a Giant Moat for NVIDIA

Despite everyone's focus on hardware, the software of AI is what protects NVIDIA



Issues with MLPs

- 1. Resource Intensive
- 2. Difficult to incorporate certain types of information



What would happen if we permuted the ordering of the pixels?

https://medium.com/towards-data-science/creating-a-multilayer-perceptron-mlp-classifier-model-to-identify-handwritten-digits-9bac1b16fe10

(24, 5)(13, 2)(4, 0) (21, 11) • • • Image is transformed to vector of pixels

What would happen if we permuted the ordering of the pixels?

Will the training of the neural network differ?

No! MLPs do not use spatial information, it does not matter which order the pixels are fed in so long as it is the same ordering for every input



Isn't this actually a hard problem that we are trying to learn?

Limitations of Full Connections for MNIST

Suppose we've got a well-trained MNIST classifier...



Limitations of Full Connections for MNIST



Limitations of Full Connections for MNIST

If we shift the digit to the right, then a different set of this pixel gets weight 0.6 weights becomes relevant $\Box \rightarrow$ etwork might have trouble classifying this as a 1... this pixel gets weight 0.1 \sim #1 encoded as □ this pixel gets weight 0.9 Can you tell this is a 1?

This would **not** be a problem for the human visual system



This would **not** be a problem for the human visual system



Translational Invariance

- To make a neural net f robust in this same way, it should ideally satisfy *translational invariance*: f(T(x)) = f(x), where
 - x is the input image
 - T is a translation (i.e. a horizonal and/or vertical shift)



Fully Connected Nets are *not* Translationally Invariant



Sum of these three: $0.6 \cdot 0.8 + 0.1 \cdot 0 + 0.9 \cdot 1 = 1.38$

Sum of these three: $0.6 \cdot 0 + 0.1 \cdot 0.4 + 0.9 \cdot 0 = 0.4$

How can we change a fullyconnected network to account for spatial information?

MLPs (also called fully-connected networks) have weights from every pixel to every neuron



Patches: Pixels close to each other



Advantages of Not Fully Connected Layers

- Fewer weights → Faster?
- The outputs of neurons are "features" for local "patches"
- Incorporates spatial information (pixels that are close together matter)



Disadvantages of Not Fully Connected Layers

- What happens if the image is Translated?
- The patches on the right side were never trained with 5's in that side.



Even though we include spatial *information*, we still don't have spatial *reasoning*. (Can't recognize a shifted 5 is still a 5)

What if we used the same weights for each patch? (Weight Sharing)

The Main Building Block: Convolution

Convolution is an operation that takes two inputs:

(1) An image (2D – B/W)

(2) A filter (also called a kernel)





2D array of numbers; could be any values



Overlay the filter on the image



Sum up multiplied values to produce output value



Move the filter over by one pixel





output

Move the filter over by one pixel

image

211170000-1-1-10514

output



Repeat (multiply, sum up)

image

 2
 0x1
 1x1
 3x1

 7
 1x0
 1x0
 0x0

 0
 2x-1
 5x-1
 0x-1

 0
 5
 1
 4

output



Repeat (multiply, sum up)



Repeat...



Repeat...



In summary:



Handmade Kernels and Filters



Identity kernel



Box blur

| 1 | -1] | 0 |
|---|-----|----|
| 3 | -1 | -1 |
| 1 | -1 | 0 |

Edge detection

 $egin{array}{cccc} -1 & 5 & -1 \ 0 & -1 & 0 \end{array}$

| Sharpen | kernel |
|---------|--------|
|---------|--------|

| | [1 | 4 | 6 | 4 | 1] |
|-----------------|----|----------|-----------|----------|----|
| $\frac{1}{256}$ | 4 | 16 | 24 | 16 | 4 |
| | 6 | 24 | 36 | 24 | 6 |
| 200 | 4 | 16 | 24 | 16 | 4 |
| | 1 | 4 | 6 | 4 | 1 |

Gaussian blurr kernel

| Operation | Kernel ω | Image result g(x,y) |
|--|---|---------------------|
| Identity | $\left[\begin{array}{rrrr} 0 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 0 \end{array}\right]$ | |
| Ridge or edge detection | $\begin{bmatrix} 0 & -1 & 0 \\ -1 & 4 & -1 \\ 0 & -1 & 0 \end{bmatrix}$ | |
| | $\begin{bmatrix} -1 & -1 & -1 \\ -1 & 8 & -1 \\ -1 & -1 & -1 \end{bmatrix}$ | |
| Sharpen | $\begin{bmatrix} 0 & -1 & 0 \\ -1 & 5 & -1 \\ 0 & -1 & 0 \end{bmatrix}$ | |
| Box blur (normalized) | $\frac{1}{9} \begin{bmatrix} 1 & 1 & 1 \\ 1 & 1 & 1 \\ 1 & 1 & 1 \end{bmatrix}$ | - |
| Gaussian blur 3 × 3 (approximation) | $\frac{1}{16} \left[\begin{array}{rrrr} 1 & 2 & 1 \\ 2 & 4 & 2 \\ 1 & 2 & 1 \end{array} \right]$ | |

What Comes Next?

Can we learn a filter for our images rather than "hand crafting" one?

Recap

Fully Connected Neural Networks for images lack spatial information

Participation Quiz #3 is up!

We can add spatial reasoning by connecting pixels that are "spatially" close

Convolutions/Filters/Kernels are a technique from image processing that combine close pixels with a linear transformation